

FIG. 1

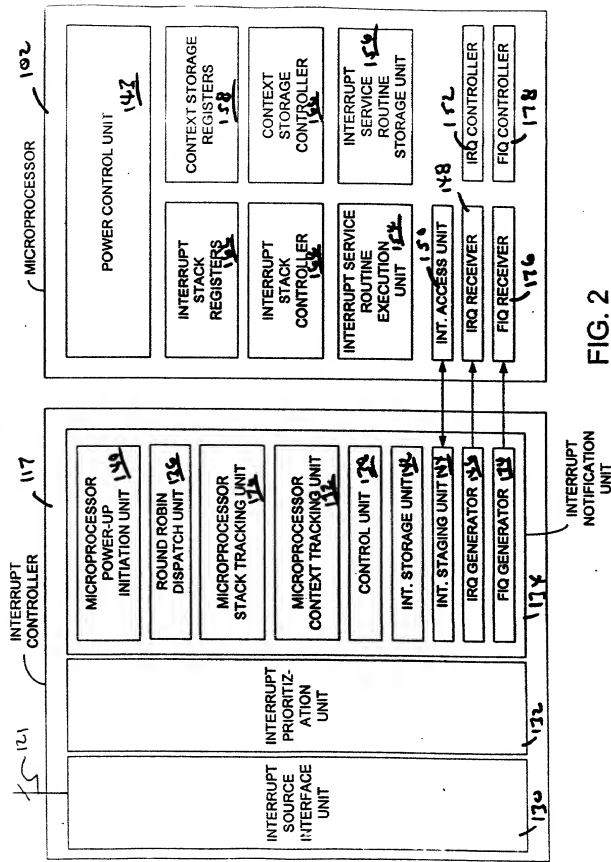


FIG. 2

INTERRUPT CONTROLLER

MICROPROCESSOR

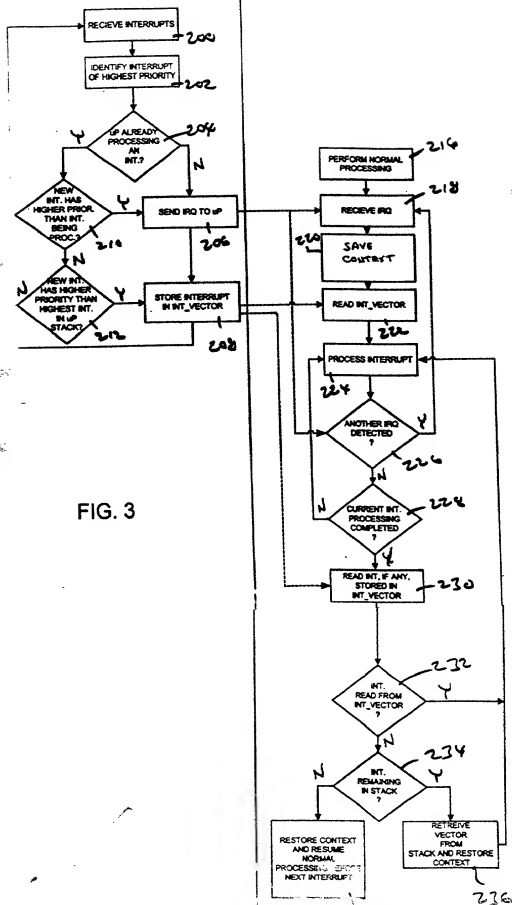
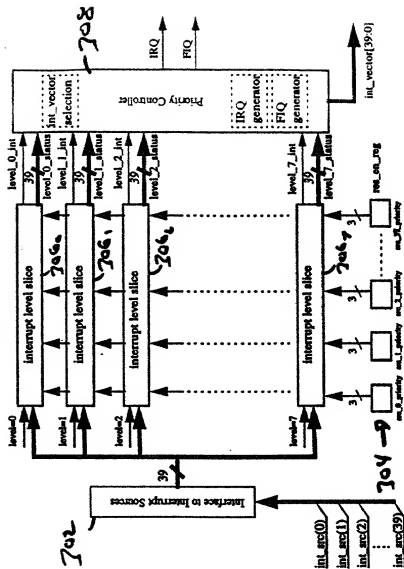


FIG. 3



4. 15. 19

300

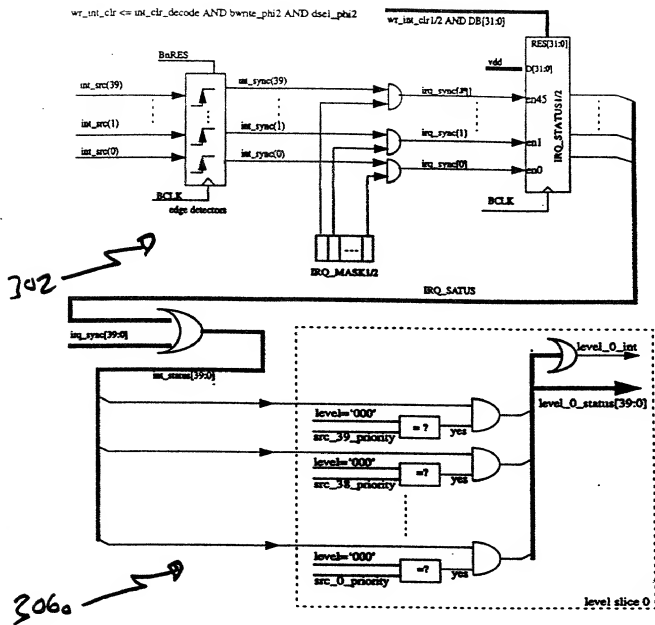


FIG. 5

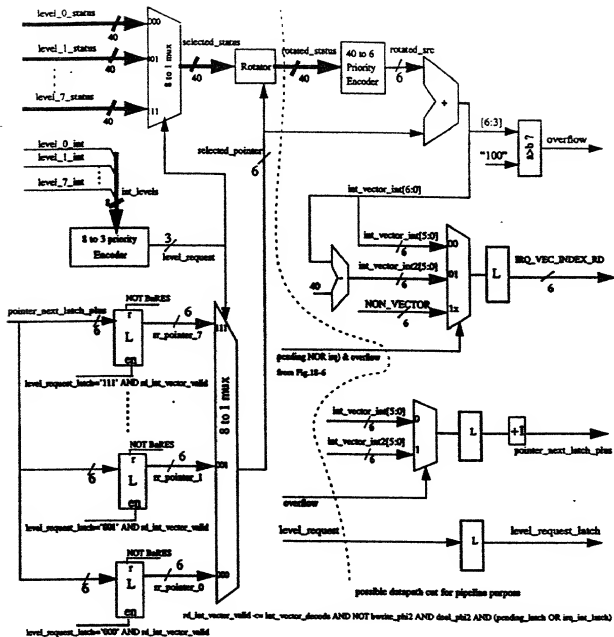


FIG. 6

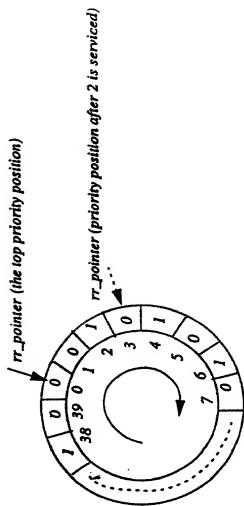


FIG. 7

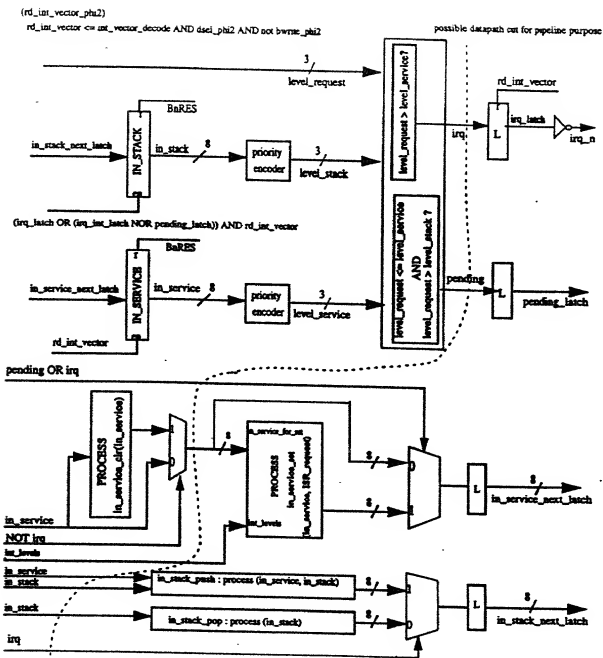


FIG. 8

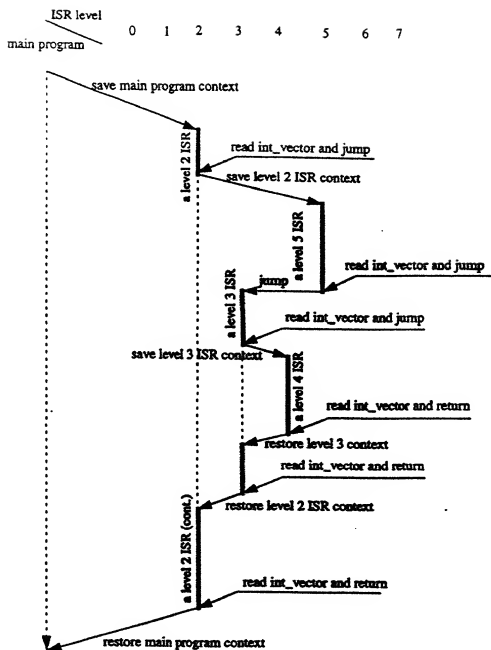


FIG. 9

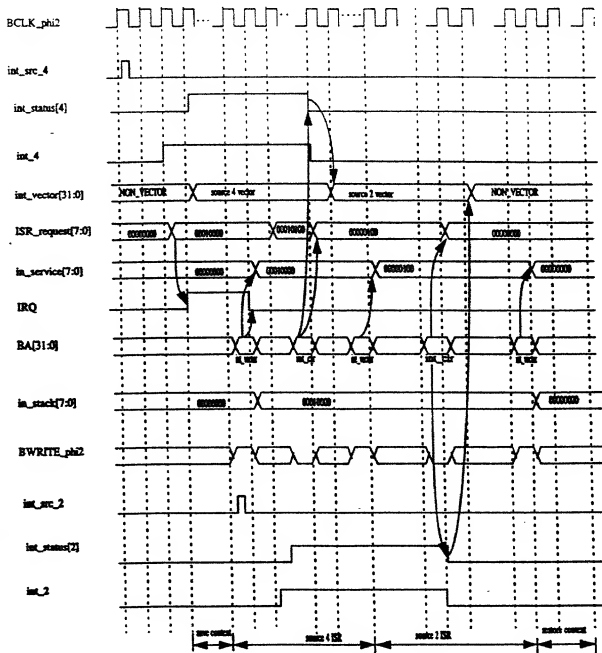


FIG. 11

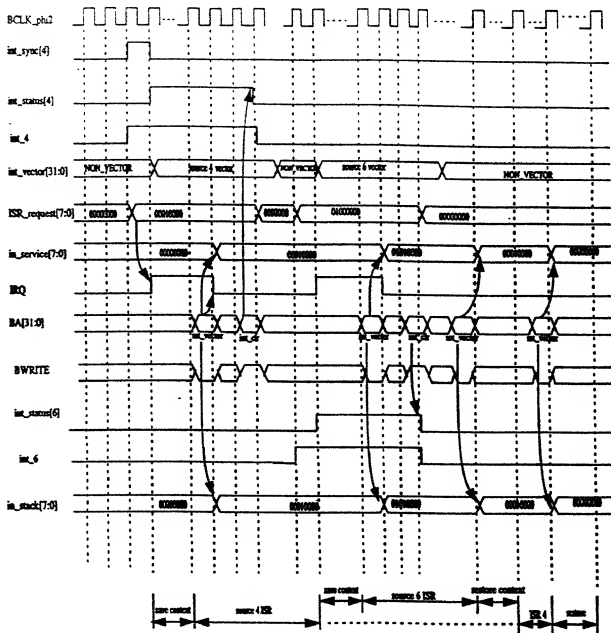


FIG. 12

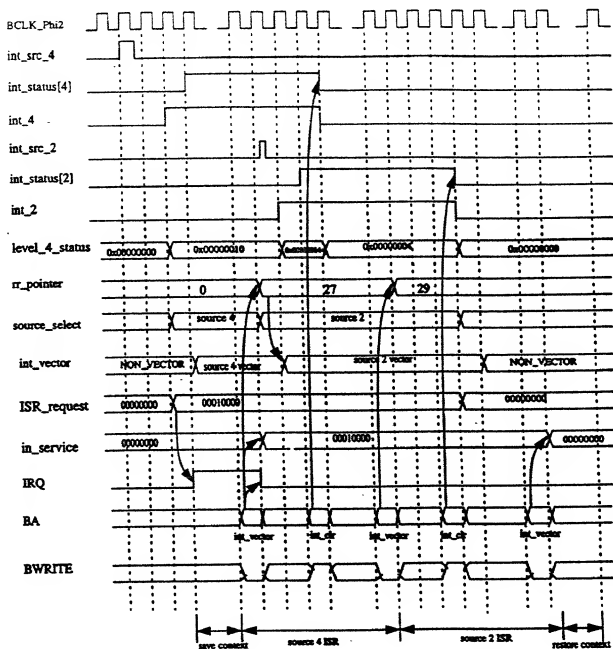


FIG. 13

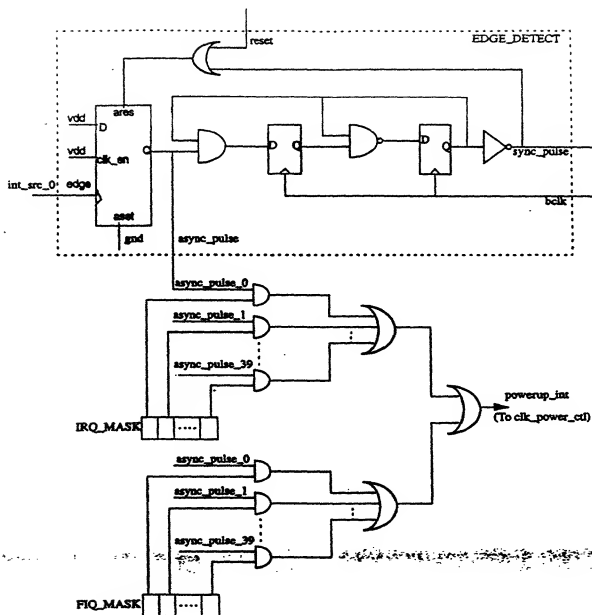


FIG. 14